IN THE CLAIMS

The following is a listing of the claims in the application with claim 1, the only claim in the application, shown as amended and with the words "WHAT IS CLAIMED IS" used as the heading.

WHAT IS CLAIMED IS:

1. (currently amended) A generator of repetitive sets of spreading sequences for the transmission of symbols by spread spectrum, characterized in that it comprises:

a) means of counting and forming an address (20) comprising:

- an input (22), receiving the symbols to be processed (Sij);

- a synchronization input (24), receiving pulses (Hs) synchronized with the symbols;

- means of counting the number of received symbols and forming an address (AB), ~~this~~ the address comprising a first part (A) composed of a number q of bits, where q is the number of bits in each symbol, and a second part (B) composed of a number r of bits where $r = \log_2 S$, and where S denotes the number of sequences in a set of sequences, the address (AB) thus comprising a number p of bits where $p = 1+\log_2 S$;

- an output (23) on which this address (AB) can be collected, for each input of ~~a~~ each symbol (sij) applied to the means (20);

b) a sequences table (30) comprising a number L of blocks (where $L=2^q$), each block memorizing ~~a~~ the set of S sequences, ~~this~~ the sequence table being addressed by the address output by the counting and addressing means, the first part (A) of the address selecting one set among the L blocks and the second part (B) selecting one sequence among the S sequences in ~~this~~ the one set.